

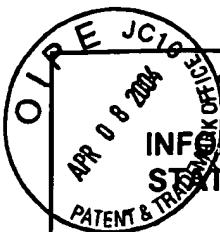
Please type a plus sign (+) inside this box **TM**



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Sheet	1	of	2	Attorney Docket Number	SPLX.P0091
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U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	U.S. Patent Document Number	Kind Code ⁴ (if known)	Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.	2002/0157063	A1	10-24-2002	Besson	
	2.	2002/0178432	A1	11-28-2002	Kim et al.	
	3.	2003/0145288	A1	07-31-2003	Wang et al.	
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	20.	6,470,486	B1	10-22-2002	Knapp	

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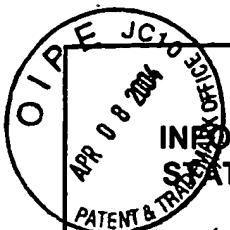
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	26.	6,618,835	B1	09-09-2003	Garlapati et al.	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				T ⁵
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	28.	NN9411291, "Design of Portable Library using Parameterized Cells", IBM Technical Disclosure Bulletin, Vo. 37, No. 11, pp. 291-292 (4 pages).				
	29.	Markovic et al., "FPGA to ASIC Conversion Design Methodology with the Support for Fast Retargetting to Different CMOS Implementation Technologies", Proceedings of 2002 22 nd International Conference on Microelectronics", Vol. 2, 14 May 2000, pp. 703-706.				
	30.	Ruiz et al., "Design and Prototyping of DSP Custom Circuits Based on a Library of Arithmetic Components", Vol. 1, 9 November 1997, pp. 191-196.				
	31.	Tamiya, Y., "Delay Estimation for Technology Independent Synthesis", 1997 Proceedings of the ASP-DAC '97, Asia and South Pacific Design Automation Conference, 28 January 1997, pp. 31-36.				
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Substitute for form 1449A/PTO				Application Number	10/061,459
				Filing Date	1/31/02
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2825
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	SPLX.P0091

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	2.	5,526,276		06-11-1996	Cox et al
	3.	5,752,000		05-12-1998	McGeer et al
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	6.	6,134,705		10-17-2000	Pedersen et al
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	2.	5,537,341		07-16-1996	Rose et al.	
	3.	5,696,974		12-09-1997	Agrawal et al.	
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VAS	7.	6,301,696	B1	11-09-2001	Lien et al.	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁴
VAS	8.	Crastes, M. et al., A Technology Mapping Method Based on Perfect and Semi-Perfect Matchings, Design Automation Conference, 1991, 28 th ACM/IEEE, June 17-21, 1991, pp. 93-98.	
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